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1   Level set and PDE methods for computer graphics
David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro, Ross Whitaker
August 2004 SIGGRAPH '04: ACM SIGGRAPH 2004 Course Notes
Publisher: ACM
Full text available: 📄 pdf(17.07 MB)   Additional Information: full citation, abstract, cited by

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Level set methods, an important class of partial differential equation (PDE) methods,
define dynamic surfaces implicitly as the level set (iso-surface) of a sampled, evolving
nD function. The course begins with preparatory material that introduces the ...

2   Rotation invariant distance measures for trajectories
Michail Vlachos, D. Gunopulos, Gautam Das
August 2004 KDD '04: Proceedings of the tenth ACM SIGKDD international conference on
Knowledge discovery and data mining
Publisher: ACM
Full text available: 📄 pdf(305.87 KB)   Additional Information: full citation, abstract, references, cited by, index terms

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For the discovery of similar patterns in 1D time-series, it is very typical to perform a
normalization of the data (for example a transformation so that the data follow a zero
mean and unit standard deviation). Such transformations can reveal latent ...

Keywords: rotation invariance, time warping, trajectories

3   External memory algorithms and data structures: dealing with massive data
Jeffrey Scott Vitter
June 2001 ACM Computing Surveys (CSUR), Volume 33 Issue 2
Publisher: ACM
Full text available: 📄 pdf(828.46 KB)   Additional Information: full citation, abstract, references, cited by, index terms

Data sets in large applications are often too massive to fit completely inside the computers internal memory. The resulting input/output communication (or I/O) between fast internal memory and slower external memory (such as disks) can be a major performance ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hierarchical memory, multidimensional access methods, multilevel memory, online, out-of-core, secondary storage, sorting

4 Code size minimization and retargetable assembly for custom EPIC and VLIW instruction formats
Shail Aditya, Scott A. Mahlke, B. Ramakrishna Rau
October 2000 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 5 Issue 4
Publisher: ACM
Full text available: pdf(568.33 KB) Additional Information: full citation, abstract, references, index terms

PICO is a fully automated system for designing the architecture and the microarchitecture of VLIW and EPIC processors. A serious concern with this class of processors, due to their very long instructions, is their code size. One focus of this paper is ...

Keywords: EPIC, VLIW, code size minimization, custom templates, design automation, instruction format design, noop compression, retargetable assembly

5 Shape-based retrieval and analysis of 3D models
Thomas Funkhouser, Michael Kazhdan
August 2004 SIGGRAPH '04: ACM SIGGRAPH 2004 Course Notes
Publisher: ACM
Full text available: pdf(12.56 MB) Additional Information: full citation, abstract

Large repositories of 3D data are rapidly becoming available in several fields, including mechanical CAD, molecular biology, and computer graphics. As the number of 3D models grows, there is an increasing need for computer algorithms to help people find ...

6 An evaluation of multi-resolution storage for sensor networks
Deepak Ganesan, Ben Greenstein, Denis Perelyubskiy, Deborah Estrin, John Heidemann
November 2003 SenSys '03: Proceedings of the 1st international conference on Embedded networked sensor systems
Publisher: ACM
Full text available: pdf(299.34 KB) Additional Information: full citation, abstract, references, cited by

Wireless sensor networks enable dense sensing of the environment, offering unprecedented opportunities for observing the physical world. Centralized data collection and analysis adversely impact sensor node lifetime. Previous sensor network research ...

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